

Google Scholar search - 8/22/06

"register transfer language" lock step
 lockstep processors "register transfer
 "register transfer" lockstep processors
 lockstep block model
 lockstep cores "register transfer
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 model "register transfer
 synchronized cores "register transfer
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 "lockstep processor"
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 "register transfer" "synchronized processors"

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lockstep processors divergence simulation
 lockstep processors divergence verifier
 lockstep processors rtl simulation
 lockstep processors verification model
 modeling "lockstep processor"
 modeling lockstep processors divergence
 multiprocessor model verify synchronization
 processor model verify synchronization
 processors "verify synchronization
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 register transfer "synchronized processors
 simulating "lockstep cores"
 simulating "lockstep processor"
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 simulating lockstep processor
 simulation lockstep verification
 simulation verifying "synchronized processors
 verification "lock-step cores"
 verification "lock-step processors"
 verification "synchronized processor
 verification lockstep design
 verification lockstep processor design
 verifying lockstep processors simulation
 verification "lockstep cores"
 "redundant processors" rtl
 "redundant processors" verification model